

WEST Search History

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DATE: Saturday, August 05, 2006

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L45	110 and bidirection\$	27
<input type="checkbox"/>	L44	135 and bidirection\$	0
<input checked="" type="checkbox"/>	L43	120 near5 first near5 second	2
<input type="checkbox"/>	L42	20040105292 and phase	1
<input type="checkbox"/>	L41	20040105292 and align\$	0
<input type="checkbox"/>	L40	20040105292 and align\$	0
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L39	20040105292 and align\$	0
		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input checked="" type="checkbox"/>	L38	('6668292')[URPN]	3
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L37	(5623522 6009107 5307381 5909563 6249555 5058132 6026133 4929854 5256994 5748684 5825834 6038254 4970405 4989175 3725793 6431442 6044421 6240151)![PN]	18
		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L36	('6668292')[PN]	1
<input type="checkbox"/>	L35	6668292.pn.	1
<input checked="" type="checkbox"/>	L34	6393502[uref]	6
<input type="checkbox"/>	L33	6668292[uref]	3
<input checked="" type="checkbox"/>	L32	6668292[uref]	3
<input type="checkbox"/>	L31	L29 and (first near5 second near5 clock)	31
<input checked="" type="checkbox"/>	L30	L29 and 110	1
<input type="checkbox"/>	L29	elamin-\$xa. or elamin-\$xp.	417
<input type="checkbox"/>	L28	l25 or l26 or l27	11
<input checked="" type="checkbox"/>	L27	6259650[uref]	2
<input type="checkbox"/>	L26	6212122[uref]	4
<input checked="" type="checkbox"/>	L25	6134155[uref]	5
<input type="checkbox"/>	L24	5644604[uref]	14
<input checked="" type="checkbox"/>	L23	5644604[uref]	14
<input checked="" type="checkbox"/>	L22	L20.ti,ab.	2
<input type="checkbox"/>	L21	L20 same (first near5 second near5 clock)	1

<input checked="" type="checkbox"/>	L20	data phase signal	105
<input checked="" type="checkbox"/>	L19	L10 near10 (multiplex\$3 or mux)	5
<input checked="" type="checkbox"/>	L18	L10 near7 (common or shared)	7
<input checked="" type="checkbox"/>	L17	l8 and l10	0
<input checked="" type="checkbox"/>	L16	5689643[uref]	2
<input checked="" type="checkbox"/>	L15	5689643.pn.	1
<input checked="" type="checkbox"/>	L14	l10 same dram	1
<input checked="" type="checkbox"/>	L13	l10.ti,ab.	26
<input checked="" type="checkbox"/>	L12	L10 same ((common or shared) near5 (connect\$3 or path or bus))	1
<input checked="" type="checkbox"/>	L11	L10 near7 (common or shared) near5 (connect\$3 or path or bus)	0
<input checked="" type="checkbox"/>	L10	timing signal near5 first near5 second near5 clock	370
<input checked="" type="checkbox"/>	L9	6654270[uref]	3
<input checked="" type="checkbox"/>	L8	6654270.pn.	1
<input checked="" type="checkbox"/>	L7	20040105292 and data phase	1
<input checked="" type="checkbox"/>	L6	5544082.pn. and (toler\$6)	1
<input checked="" type="checkbox"/>	L5	5544082.pn. and (speed)	0
<input checked="" type="checkbox"/>	L4	5544082.pn. and (frequency or rate)	1
<input checked="" type="checkbox"/>	L3	5544082.pn. and clock	1
<input checked="" type="checkbox"/>	L2	5544082.pn.	1
<input checked="" type="checkbox"/>	L1	20040235536	1

END OF SEARCH HISTORY